ABSTRACT OF THE DISCLOSURE

A method of initializing a computer system equipped with a debugging system. The computer system has a ROM coupled to an expansion bus and stores a first BIOS code,

5 and the debugging system is coupled to a peripheral bus. The method comprises the steps of routing data requests directed to the ROM to a local bus by a CPU, transferring the data requests from the local bus to the peripheral bus via a first bridge, switching a second bridge into normal

10 mode wherein the second bridge is enabled to respond to data requests on the peripheral bus with the first BIOS code stored in ROM to be loaded in the CPU, and switching the second bridge to debugging mode wherein the second bridge is disabled from responding to data requests on the peripheral

15 bus, instead, the debugging system responds to data requests with the second BIOS code to be loaded in the CPU.